

Mr. Matt Longbrake is a Senior Computer Engineer at Sensors Directorate, Air Force Research Laboratory (AFRL), Wright-Patterson Air Force Base (WPAFB), Ohio. His current work focuses on maturing direct-RF sampling technology into subsystems for Air Force applications. He previously served as part of the government Independent Verification and Validation team for the DARPA Arrays at Commercial Timescales program and helped manage DARPA efforts that produced the Xilinx RFSoc and Intel Agilex 9 Direct RF products. He also developed wideband, true time-delay digital beamforming approaches for phased arrays as well as embedded control architectures for wideband analog beamsteering. Mr. Longbrake received his master's in electrical engineering from Wright State University in 2016 and his bachelor's in computer engineering from Rose-Hulman Institute of Technology in 2007.